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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **OHASHI, Hitoshi et al.**

Group Art Unit: 1762

Serial No.: 10/047,992

Examiner: **Brian K. TALBOT**

Filed: **January 17, 2002**

P.T.O. Confirmation No.: 5363

For. **METHOD AND APPARATUS FOR FORMING ELECTRIC CIRCUIT FIXING
THE SEMICONDUCTOR DEVICE USING SUBSTRATE JIG**

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: February 5, 2007

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the
Final Office Action dated **September 26, 2006** for two months, from **December 26, 2006** to
February 26, 2007.

Attached is a check including the amount of \$450.00 to cover the cost of the extension for
a large entity. In the event that any additional fees are due in connection with this paper, please
charge our Deposit Account No. 01-2340.

Respectfully submitted,

**ARMSTRONG, KRATZ, QUINTOS,
HANSON & BROOKS, LLP**

Handwritten signature of William L. Brooks
William L. Brooks
Attorney for Applicant
Reg. No. 34,129

02/06/2007 SZEWDIE1 00000044 10047992
02 FC:1252 450.00 OP

WLB/ak
Atty. Docket No. **020052**
Suite 1000
1725 K Street, N.W.
Washington, D.C. 20006
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PATENT TRADEMARK OFFICE